LCPC Interface

LPC stand for Low Pin Count - it is the chip used to connect all of the "legacy" PC components on motherboards. For example it will control the PS/2, floppy, parallel and serial ports.

The LPC bus was introduced by Intel in 1998 as a software-compatible substitute for the Industry Standard Architecture (ISA) bus. It resembles ISA to software, although physically it is quite different.

It details the goals, signal definitions, and functionality requirements for the LPC interface, which includes support for memory, I/O, DMA, and bus master cycles. The document also specifies ...

This document contains a specification for a new low pin count bus interface, called LPC. The target audiences for this document are system and component designers.

Most PC motherboards with an LPC bus have either a Platform Controller Hub (PCH) or a southbridge chip, which acts as the host and controls the LPC bus. All other devices connected to ...

With Microsoft Windows, LPC (Local Procedure Call) is an IPC (InterProcess Communication) mechanism that allows software to call routines used in other processes.

Data Sheet The SST49LF080A flash memory device is designed to interface with the LPC bus for PC and Internet Appliance application in compliance with Intel Low Pin Count (LPC) Interface Specifi. ...

As the needs of the computing industry continue to evolve, the more flexible and efficient Enhanced Serial Peripheral Interface (eSPI) bus was introduced to overcome the limitations of the LPC bus.

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