

With a clear understanding of IoT system entities and a reference architecture, we can now explore the ST AIoT Craft IoT API interface, summarized with the following picture:

The bridge API initializes the microcontroller of the STLINK-V3 subsystem and controls the communication through its I2C, SPI, and CAN interfaces. It also allows the configuration of up to four ...

This article describes the embedded inference client API and associated runtime software stack. The C-application must use this stack to utilize the specialized or configuration files generated by the ST ...

In this section the various steps required to initialize, setup and run a network model will be shown. For each step the legacy code will be presented followed by the equivalent one using new ST Edge AI ...

The bridge API initializes the microcontroller of the STLINK-V3 subsystem and controls the communication through its interfaces: I2C, SPI, CAN, and optionally CAN FD.

Maybe ST-LINK firmware once had provision to access the target UART interface via its main USB function. You can tell which VCP on the host corresponds to the ST-LINK (if there are ...

The purpose of this document is to explain the architecture and the implementation of the I2C CPAL (Communication Peripheral Application Library). CPAL is a library providing high layer API for I2C ...

The STM32 SPI controller driver offers an ST SPI internal peripheral abstraction layer to the spi-core. It defines all the SPI transfer methods to be used by the SPI core base.

It provides a full description of how to integrate a free middleware TCP/IP stack using STM32Cube HAL drivers into an embedded application based on STM32 microcontroller. The middleware TCP/IP ...

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